

CS 636: Shared-Memory Synchronization

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What is the Desired Property?

```
1  class Set {
2      final Vector elems = new Vector();
3
4      void add(Object x) { // Free of data races
5          if (!elems.contains(x))
6              elems.add(x);
7      }
8  }
9
10 class Vector {
11     synchronized void add(Object o) { ... }
12     synchronized boolean remove(Object o) { ... }
13     synchronized boolean contains(Object o) { ... }
14 }
```

What is the Desired Property?

```
1  class Set {
2      final Vector elems = new Vector();
3
4      void add(Object x) { // Free of data races
5          if (!elems.contains(x)) } atomic
6              elems.add(x);
7      }
8  }
9
10 class Vector {
11     synchronized void add(Object o) { ... }
12     synchronized boolean remove(Object o) { ... }
13     synchronized boolean contains(Object o) { ... }
14 }
```

Synchronization Patterns

Mutual exclusion – updates need to be serialized

```
1 bool lock = false;
```

```
1 lock_acquire():  
2   while TAS(&lock)  
3     // spin
```

```
1 lock_release():  
2   lock = false;  
3
```

Conditional synchronization – events need to occur in a specified order

```
1 while !condition  
2   // spin
```

Global synchronization – control the number of simultaneous accesses to a shared resource

Desired Synchronization Properties

Mutual exclusion

- Critical sections on the same lock from different threads do not overlap
- Safety property

Deadlock freedom

- If some threads attempt to acquire the `lock()`, then some thread should be able to acquire the lock
- Individual threads may starve
- Liveness property

Starvation free

- Every thread that acquires a lock eventually releases it
- A lock acquire request must eventually succeed within bounded steps
- Implies deadlock freedom

Classic Mutual Exclusion Algorithms

LockOne: What could go wrong?

```
1 class LockOne implements Lock {
2     private boolean[] flag = new boolean[2];
3     public void lock() {
4         int i = ThreadID.get()
5         flag[i] = true;
6         j = 1-i;
7         while (flag[j]) {}
8     }
9     public void unlock() {
10        int i = ThreadID.get()
11        flag[i] = false;
12    }
13 }
```

- LockOne satisfies mutual exclusion
- LockOne fails deadlock-freedom, concurrent execution can deadlock

LockTwo: What could go wrong?

```
1  class LockTwo implements Lock {
2      private int victim;
3      public void lock() {
4          int i = ThreadID.get();
5          victim = i;
6          while (victim == i) {}
7      }
8
9      public void unlock() {}
10 }
```

- LockTwo satisfies mutual exclusion
- LockTwo fails deadlock-freedom, sequential execution deadlocks

Peterson's Algorithm

```
1 class PetersonLock {
2     private boolean[] flag = new boolean[2];
3     private int victim;
4
5     public void lock() {
6         int i = ThreadID.get();
7         int j = 1-i;
8         flag[i] = true;
9         victim = i;
10        while (flag[j] && victim == i) {}
11    }
12
13    public void unlock() {
14        int i = ThreadID.get();
15        flag[i] = false;
16    }
17 }
```

Peterson's Algorithm

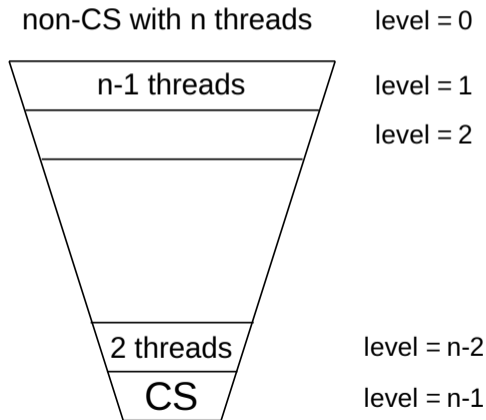
```
1 class PetersonLock {
2     private boolean[] flag = new boolean[2];
3     private int victim;
4
5     public void lock() {
6         int i = ThreadID.get();
7
8         // ... (omitted code) ...
9
10    }
11
12
13    public void unlock() {
14        int i = ThreadID.get();
15        flag[i] = false;
16    }
17 }
```

- Is this algorithm correct (i.e. satisfies mutual exclusion) under sequential consistency?
- What if we do not have sequential consistency?

Filter Lock for n Threads

Filter lock is a generalization of Peterson's lock to $n > 2$ threads

- There are $n-1$ waiting rooms called "levels"
- At least one thread trying to enter a level succeeds
- One thread gets blocked at each level if many threads try to enter



Filter Lock

```
1 class FilterLock {
2     int[] level;
3     int[] victim;
4     public FilterLock() {
5         level = new int[n];
6         victim = new int[n];
7         for (int i=0; i<n; i++)
8             level[i] = 0;
9     }
10    public void unlock() {
11        int me = ThreadID.get();
12        level[me]= 0;
13    }
14
```

```
15    public void lock() {
16        int me = ThreadID.get();
17        // Attempt to enter level i
18        for (int i=1; i<n; i++) {
19            // visit level i
20            level[me] = i;
21            victim[i] = me;
22            // spin while conflict exists
23            while (( $\exists$ k  $\neq$  me)
24                level[k]  $\geq$  i && victim[i]
25                    == me) {}
26        }
27    }
```

Fairness

Starvation freedom is good, but maybe threads should not wait too much

- For example, it would be great if we could order threads by the order in which they performed the first step of the `lock()` method

Bounded Waiting

- Divide the `lock()` method into two parts
 - (i) Doorway interval (DA) finishes in finite steps
 - (ii) Waiting interval (WA) may take unbounded steps
- A lock is first-come first-served if $D_A^j \rightarrow D_B^k$, then $CS_A^j \rightarrow CS_B^k$

r-bounded waiting

For threads A and B, if $D_A^j \rightarrow D_B^k$, then $CS_A^j \rightarrow CS_B^{k+r}$

Lamport's Bakery Algorithm

```
1  class Bakery implements Lock {
2      boolean[] flag;
3      Label[] lbl;
4      public void unlock() {
5          flag[ThreadID.get()] = false;
6      }
7      public Bakery(int n) {
8          flag = new boolean[n];
9          lbl = new Label[n];
10         for (int i = 0; i<n; i++) {
11             flag[i] = false;
12             lbl[i] = 0;
13         }
14     }
```

Lamport's Bakery Algorithm

```
15  public void lock() {
16      int i = ThreadID.get();
17      flag[i] = true;
18      label[i] = max(label[0], ..., label[n-1]) + 1;
19      while (( $\exists$  k != i) flag[k] && (label[k], k) <<
20              (label[i], i)) {}
21  }
```

$(\text{label}[i], i) << (\text{label}[j], j)$ iff

- $\text{label}[i] < \text{label}[j]$, or
- $\text{label}[i] = \text{label}[j]$ and $i < j$

Lamport's Bakery Algorithm

```
15 public void lock() {  
16     int i = ThreadID.get();  
17     flag[i] = true;  
18     label[i] = max(label[0], ..., label[n-1]) + 1;  
19     while (( $\exists$  k != i) flag[k] && (label[k], k) <<
```

- Need to compare own label with all other threads' labels irrespective of their intent to enter the critical section
- Cost of locking increases with the number of threads

$(\text{label}[i], i) << (\text{label}[j], j)$ iff

- $\text{label}[i] < \text{label}[j]$, or
- $\text{label}[i] = \text{label}[j]$ and $i < j$

Lamport's Fast Lock

- Programs with highly contended locks are likely to not scale
- **Insight:** Ideally spin locks should be free of contention in well-designed systems, so optimize for the common case
- Idea:
 - ▶ Two lock fields x and y
 - ▶ Acquire: Thread t writes its ID to x and y and checks for intervening writes

L. Lamport. A Fast Mutual Exclusion Algorithm. TOCS, vol. 5, no. 1, pp 1–17, Jan. 1987.

Fast Mutual Exclusion, Even With Contention

Lamport's Fast Lock

```
1 class LFL implements Lock {
2     private int fast_check, slow_check;
3     boolean[] trying;
4     LFL() {
5         slow_check = 1;
6         for (int i = 0; i < n; i++)
7             trying[i] = false;
8     }
9     public void unlock() {
10        slow_check = 1;
11        trying[ThreadID.get()] = false;
12    }
13 }
```

Lamport's Fast Lock

```
14 public void lock() {
15     int self = ThreadID.get();
16 start:
17     trying[self] = true;
18     fast_lock = self;
19     if (slow_lock !=  $\perp$ ) {
20         trying[self] = false;
21         while (slow_lock !=  $\perp$ ) {}
22         goto start;
23     }
24     slow_lock = self;
```

```
25     if (fast_lock != self) {
26         trying[self] = false;
27         for (i  $\in$  T) {
28             while (trying[i] == true) {}
29         }
30         if (slow_lock != self) {
31             while (slow_lock !=  $\perp$ ) {}
32             goto start;
33         }
34     }
35 }
```

Evaluating Performance of a Lock

- Lock acquisition latency** Lock acquire should be cheap in the absence of contention
- Space overhead** Maintaining lock metadata should not impose high memory overhead
- Fairness** Processors should enter the CS in the order of lock requests
- Bus traffic** Worst case lock acquire traffic should be low
- Scalability** Latency and traffic should scale slowly with the number of processors

Practicality of Classical Mutual Exclusion Algorithms

A write (regular memory store) by a thread to a memory location can be overwritten without any other thread seeing the first write

Need to read and write n distinct memory locations where n is the maximum number of concurrent threads

Lower bound on the number of required locations

Atomic Hardware Instructions

Hardware Locks

- Locks can be completely supported by hardware
- Ideas:
 - (i) Have a set of lock lines on the bus, processor wanting the lock asserts the line, others wait, priority circuit used for arbitrating
 - (ii) Special lock registers, processors wanting the lock acquire ownership of the registers

What could be some problems?

Limitations with Hardware Locks

- Waiting logic is critical for lock performance
 - ▶ A thread can (i) busy wait, (ii) block, or (iii) use a hybrid strategy
- Hardware locks are not popularly used
 - Limited in number due resource constraints
 - Inflexible in implementing wait strategies
- We continue to rely on software locks
 - ▶ Can optionally make use of hardware instructions for better performance

Common Atomic Primitives

test_and_set

X86, SPARC

```
1 bool TAS(word* loc):
2     atomic {
3         tmp := *loc;
4         *loc := true; // set
5         return tmp;
6     }
```

swap

X86, SPARC

```
1 word Swap(word* a, word b):
2     atomic {
3         tmp := *a;
4         *a := b;
5         return tmp;
6     }
```

Implement Lock Acquire with Swap

swap

X86, SPARC

```
1 word Swap(word* a, word b):  
2   atomic {  
3     tmp := *a;  
4     *a := b;  
5     return tmp;  
6   }
```

Lock acquire

```
while (swap(&lock, 1)) {}
```

```
1     addi reg, r0, 1 ; r0=0  
2 Try: xchg reg, &lck  
3     bnez reg, Try
```

Common Atomic Primitives

fetch_and_inc

uncommon

```
1 int FAI(int* loc):  
2     atomic {  
3         tmp := *loc;  
4         *loc := tmp+1;  
5         return tmp;  
6     }
```

fetch_and_add

uncommon

```
1 int FAA(int* loc, int n):  
2     atomic {  
3         tmp := *loc;  
4         *loc := tmp+n;  
5         return tmp;  
6     }
```

C++ 11 onward provides `std::atomic<T>::fetch_add()`

Common Atomic Primitives

fetch_and_inc

uncommon

fetch_and_add

uncommon

```
1 int FAI(int* loc):  
2     atomic {  
3     tmp := *loc:  
4     *loc  
5     retu  
6 }
```

```
1 int FAA(int* loc, int n):  
2     atomic {  
3     tmp := *loc:  
6 }
```

How can we implement a mutual exclusion lock with FAI?

C++ 11 onward provides `std::atomic<T>::fetch_add()`

Compare-and-Swap (CAS) Instruction

CAS

X86, IA_64, SPARC

```
1 bool CAS(word* loc, word old, word new):  
2     atomic {  
3         res := (*loc == old);  
4         if (res)  
5             *loc := new;  
6         return res;  
7     }  
8 }
```

Lock acquire

```
1     addi reg1, r0, 0x0 /*reg1=0*/  
2     addi reg2, r0, 0x1 /*reg2=1*/  
3 Lock: lock compxchgl reg1, reg2, &lck  
4     bnez reg2, Lock
```

Compare-and-Swap (CAS) Instruction

CAS

X86, IA_64, SPARC

```
1 bool CAS(word* loc, word old, word new):
2     atomic {
3         res := (*loc == old);
4         if (res)
5             *loc := new;
6         return res;
7     }
8 }
```

How can you implement `fetch_and_func()` with CAS?

Lock acquire

```
1     addi reg1, r0, 0x0 /*reg1=0*/
2     addi reg2, r0, 0x1 /*reg2=1*/
3 Lock: lock cmpxchgl reg1, reg2, &lck
4     bnez reg2, Lock
```

Load Linked (LL)/Store Conditional (SC) Instructions

LL/SC

POWER, MIPS, ARM

```
1 word LL(word* a):
2     atomic {
3         remember a;
4         return *a;
5     }
6
7 bool SC(word* a, word w):
8     atomic {
9         res := (a is remembered, and has not been evicted
10                since LL)
11         if (res)
12             *a = w;
13         return res;
14     }
```

Load Linked (LL)/Store Conditional (SC) Instructions

LL/SC

POWER, MIPS, ARM

```
1 word LL(word* a):
2     atomic {
3         remember a;
4         return *a;
5     }
6
7 bool SC(word* a, word w):
8     atomic {
9         res = fetch_and_func(*a, w);
10        if (!res) return false; // cache evicted
11        with LL/SC?
12        *a = w;
13        return res;
14    }
```

How can you implement `fetch_and_func()` with LL/SC?

Load Linked (LL)/Store Conditional (SC) Instructions

LL/SC

POWER, MIPS, ARM

```
1 word LL(word* a):  
2     atomic {  
3         remember a;  
4         return *a;  
5     }  
6  
7 bool SC(word* a, word w):  
8     atomic {  
9         res := (a is remembered, and has not been evicted  
10  
11         if (res)  
12             *a = w;  
13         return res;  
14     }
```

How can you implement CAS with LL/SC?

ABA Problem

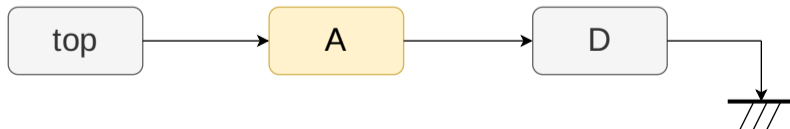
Stack Data Structure

push

```
1 void push(node** top, node* new):  
2   node* old  
3   repeat  
4     old := *top  
5     new->next := old  
6   until CAS(top, old, new)  
7  
8
```

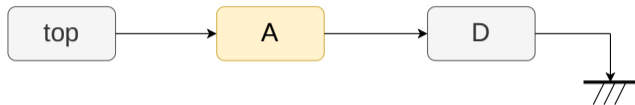
pop

```
1 node* pop(node** top):  
2   node* old, new  
3   repeat  
4     old := *top  
5     if old = null return null  
6     new := old->next  
7   until CAS(top, old, new)  
8   return old
```



Concurrent Modifications to the Stack

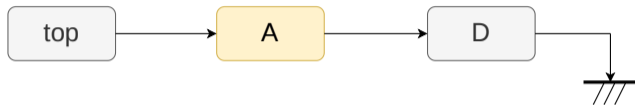
Thread 1 is executing pop(A)



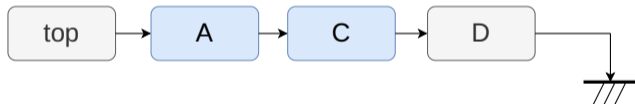
Thread 1 sees top points to A, but gets delayed while executing pop(A)

Concurrent Modifications to the Stack

Thread 1 is executing pop(A)

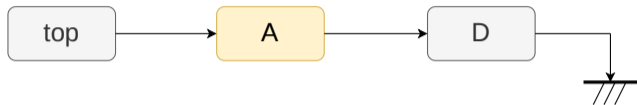


Other threads execute pop(A), push(C), and push(A)

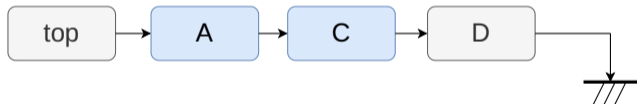


ABA Problem

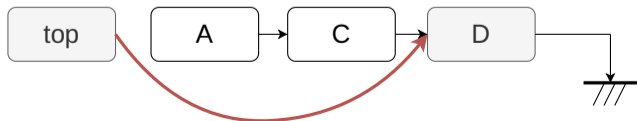
Thread 1 is executing pop(A)



Other threads execute pop(A), push(C), and push(A)



Thread 1's CAS succeeds



Avoiding ABA Problem using CAS

- Common workaround is to add extra “tag” to the memory address being compared
 - ▶ Tag can be a counter that tracks the number of updates to the reference
 - ▶ Can steal lower order bits of memory address or use a separate tag field if 128-bit CAS is available

Centralized Mutual Exclusion Algorithms

Test-And-Set (TAS)

- Atomically tests and sets a word
 - ▶ For example, swaps one for zero and returns the old value
 - ▶ `java.util.concurrent.AtomicBoolean::getAndSet(bool val)`
- Bus traffic?
- Fairness?

```
1 bool TAS(bool* loc) {  
2     bool res;  
3     atomic {  
4         res = *loc;  
5         *loc = true;  
6     }  
7     return res;  
8 }
```

Spin Lock with TAS

```
1 class SpinLock {  
2     bool loc = false;  
3     public void lock() {  
4         while (TAS(&loc)) {  
5             // spin  
6         }  
7     }  
8     public void unlock() {  
9         loc = false;  
10    }  
11 }
```

Spin Lock with TAS

```
1 class SpinLock {  
2     bool loc = false;  
3     public void lock() {  
4         while (TAS(&loc)) {  
5             // spin  
6         }  
7     }  
8     public void unlock() {  
9         loc = false;  
10    }  
11 }
```

- Delays processors not waiting for the lock
- Lock release can be delayed by spinners
- Does not support reader-writer locking
- No control over locking policy

Test-And-Test-And-Set

- Keep reading the memory location till the location appears unlocked
- Reduces bus traffic—why?

```
1 do {  
2     while (TATAS_GET(loc)) {}  
3 } while (TAS(loc));
```

Exponential backoff

- Adapt when to retry to reduce contention
- For example, increase the backoff with the number of unsuccessful retries (implies high contention)
 - ▶ Possibly double on each retry attempt till a given maximum

Spin Lock with TAS and Backoff

```
1  class SpinLock {
2      bool loc = false;
3      const int MIN = ...;
4      const int MUL = ...;
5      const int MAX = ...;
6      public void unlock() {
7          loc = false;
8      }
9      public void lock() {
10         int backoff = MIN;
11         while (TAS(&loc)) {
12             pause(backoff);
13             backoff = min(backoff * MUL, MAX);
14         }
15     }
16 }
```

Challenges with Exponential Backoff

Exponential backoff

- Adapt when to retry to reduce contention
- For example, increase the backoff with the number of unsuccessful retries (implies high contention)
 - ▶ Possibly double on each retry attempt till a given maximum

What can be some problems with this?

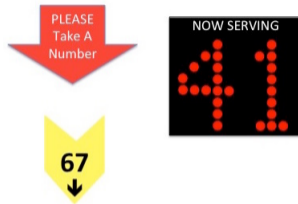
Challenges with Exponential Backoff

Exponential backoff

- Adapt when to retry to reduce contention
 - For example, increase the backoff with the number of unsuccessful retries (implies high contention)
 - ▶ Possibly double on each retry attempt till a given maximum
-
- Avoid concurrent threads getting into a lockstep, backoff for a random duration, doubling each time till a given maximum
 - Critical section is underutilized

Ticket Lock

- Grants access to threads based on FCFS
- Uses `fetch_and_inc()`

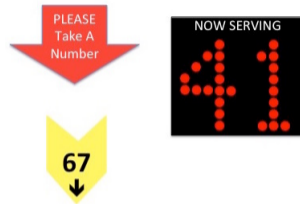


```
1 class TicketLock implements Lock {
2     int nxt_tkt = 0;
3     int serving = 0;
4     public void unlock() {
5         serving++;
6     }
```

```
7     public void lock() {
8         int my_tkt = FAI(&nxt_tkt);
9         while (serving != my_tkt) {}
10    }
11 }
12 }
```


Ticket Lock

- Grants access to threads based on FCFS
- Uses `fetch_and_inc()`



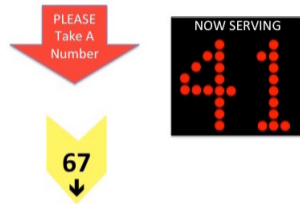
```
1 class TicketLock implements Lock {
2     int nxt_tkt = 0;
3     int serving = 0;
4     public void unlock() {
5         serving++;
6     }
```

```
7     public void lock() {
8         int my_tkt = FAI(&nxt_tkt);
9         while (serving != my_tkt) {}
10    }
11 }
```

How is this different from Bakery's algorithm?

Ticket Lock

- Grants access to threads based on FCFS
- Uses `fetch_and_inc()`



```
1 class TicketLock implements Lock {
2     int nxt_tkt = 0;
3     int serving = 0;
4     public void unlock() {
5         serving++;
6     }
```

What are some disadvantages of ticket lock?

```
7     public void lock() {
8         int my_tkt = FAI(&nxt_tkt);
9         while (serving != my_tkt) {}
10    }
11 }
12 }
```

Scalable Spin Locks

Queued Locks

Key Idea

- Instead of contending on a single “serving” variable, make threads wait in a queue (i.e., FCFS)
- Each thread knows its order in the queue

Implementations

- Implement a queue using arrays
 - ▶ Statically or dynamically allocated depending on the number of threads
- Each thread spins on its own lock (i.e., array element), and knows the successor information

Queued Lock

```
1 public class ArrayLock implements
2     Lock {
3     AtomicInteger tail;
4     volatile boolean[] flag;
5     ThreadLocal<Integer> mySlot = ...;
6     public ArrayLock(int size) {
7         tail = new AtomicInteger(0);
8         flag = new boolean[size];
9         flag[0] = true;
10    }
```

```
11 public void lock() {
12     int slot = FAI(tail);
13     mySlot.set(slot);
14     while (!flag[slot]) {}
15 }
16 public void unlock() {
17     int slot = mySlot.get();
18     flag[slot] = false;
19     flag[slot+1] = true;
20 }
21 }
```

Queued Lock

```
1 public class ArrayLock implements
2     Lock {
3     AtomicInteger tail;
4     volatile boolean[] flag;
5     ThreadLocal<Integer> mySlot = ...;
6     public ArrayLock(int size) {
7         tail = new AtomicInteger(0);
8         flag = new boolean[size];
9         flag[0] = true;
10    }
```

```
11    public void lock() {
12        int slot = FAI(tail);
13        mySlot.set(slot);
14        while (!flag[slot]) {}
15    }
16    public void unlock() {
17        int slot = mySlot.get();
18        flag[slot] = false;
19        flag[slot+1] = true;
20    }
21 }
```

What could be a few disadvantages of array-based Queue locks?

Queued Lock

```
1 public class ArrayLock implements
2     Lock {
3     AtomicInteger tail;
4     volatile boolean[] flag;
5     ThreadLocal<Integer> mySlot = ...;
6     public ArrayLock(int size) {
7         tail = new AtomicInteger(0);
8         flag = new boolean[size];
9         flag[0] = true;
10    }
```

```
11 public void lock() {
12     int slot = FAI(tail);
13     mySlot.set(slot);
14     while (!flag[slot]) {}
15 }
16 public void unlock() {
17     int slot = mySlot.get();
18     flag[slot] = false;
19     flag[slot+1] = true;
20 }
21 }
```

Can we come up with better ideas?

MCS Queue Lock

MCS Queue Lock is the state-of-art scalable FIFO locks

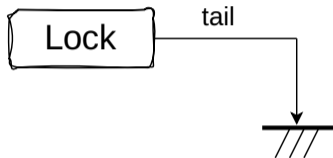
- Uses linked lists instead of arrays
- Space required to support n threads and k locks: $\mathcal{O}(n + k)$

MCS Queue Lock

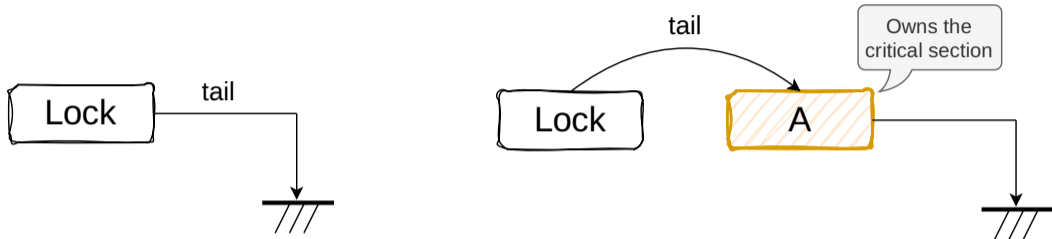
```
1  class QNode {
2      QNode next;
3      bool waiting;
4  }
5
6  public class MCSLock implements
7      Lock {
8      Node tail = null;
9      ThreadLocal<QNode> myNode = ...;
10     public void lock() {
11         QNode node = myNode.get();
12         QNode prev = swap(tail, node);
13         if (prev != null) {
14             node.waiting = true;
15             prev.next = node;
16             while (node.waiting) {}
17         }
18     }
```

```
18     public void unlock() {
19         QNode node = myNode.get();
20         QNode succ = node.next;
21         if (succ == null)
22             if (CAS(tail, node, null))
23                 return;
24         do {
25             succ = node.next;
26         } while (succ == null);
27         succ.waiting = false;
28     }
29 }
30
31
32
33
34
35
```

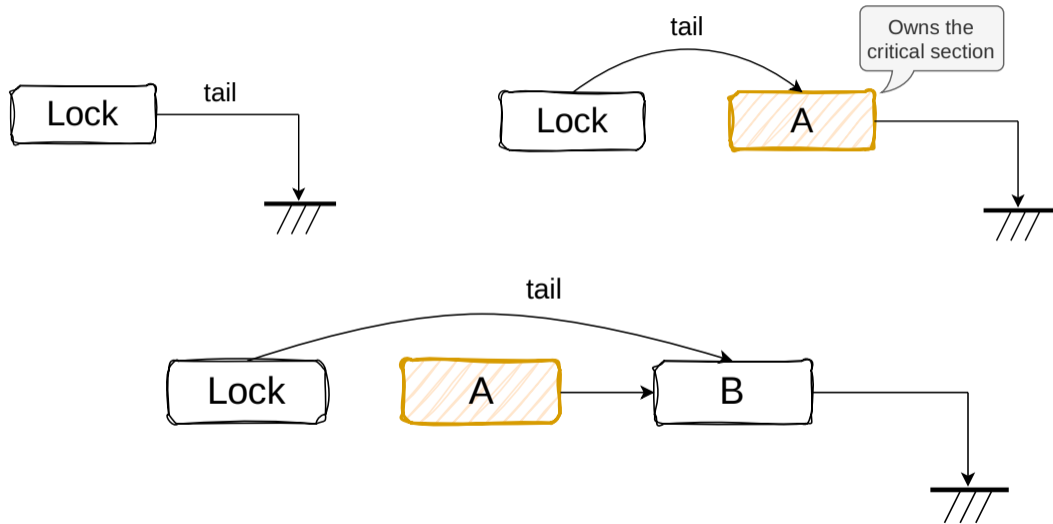
MCS Lock Operations



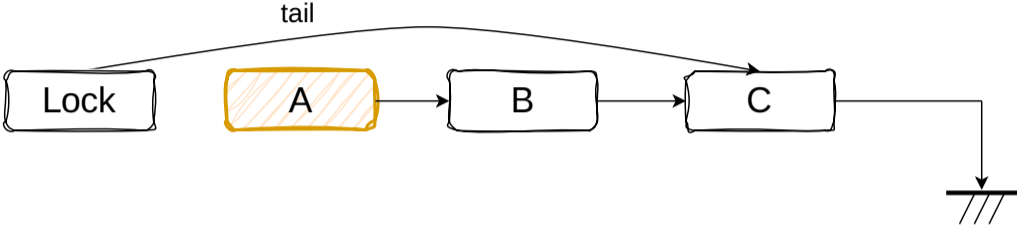
MCS Lock Operations



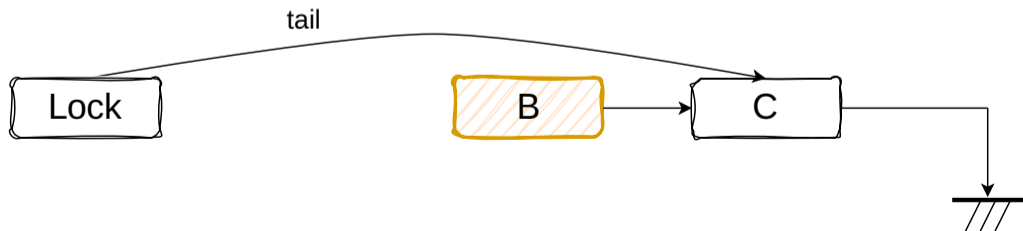
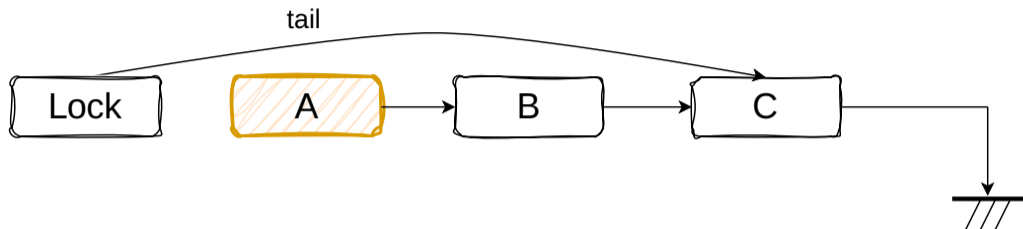
MCS Lock Operations



MCS Lock Operations



MCS Lock Operations



Properties of the MCS Lock

- Threads joining the wait queue is wait-free
 - ▶ Wait-freedom implies every operation has a bound on the number of steps it will take before the operation completes
 - ▶ Wait-freedom is the strongest non-blocking guarantee of progress
- Thread acquire locks in FIFO manner
- Minimizes false sharing and resource contention

Which Spin Lock should I use?

- Limited use of load-store-only locks
- Limited contention (e.g., few threads)
 - ▶ TAS spin locks with exponential backoff
 - ▶ Ticket locks
- High contention
 - ▶ MCS lock or other proposals like CLH lock

Miscellaneous Lock Optimizations

Reentrant Locks

Reentrant locks can be re-acquired by the owner thread

- Freed after an equal number of releases

```
1 public class ParentWidget {
2     public synchronized void doWork() {
3         ...
4     }
5 }
6
7 public class ChildWidget extends ParentWidget {
8     public synchronized void doWork() {
9         ...
10        super.doWork();
11        ...
12    }
13 }
```

Lazy Initialization In Single-Threaded Context

```
1 class Foo {  
2     private Helper helper = null;  
3  
4     public Helper getHelper() {  
5         if (helper == null)  
6             helper = new Helper();  
7         return helper;  
8     }  
9     ...  
10 }
```

Lazy initialization, correct
for single thread

Lazy Initialization In Single-Threaded Context

```
1 class Foo {  
2     private Helper helper = null;  
3  
4     public Helper getHelper() {  
5         if (helper == null)  
6             helper = new Helper();  
7         return helper;  
8     }  
9     ...  
10 }
```

What could go wrong with multiple threads?

Lazy Initialization In Multi-Threaded Context

```
1 class Foo {  
2     private Helper helper = null;  
3  
4     public Helper getHelper() {  
5         if (helper == null)  
6             helper = new Helper();  
7         return helper;  
8     }  
9     ...  
10 }
```

```
1 class Foo {  
2     private Helper helper = null;  
3  
4     public synchronized Helper getHelper() {  
5         if (helper == null)  
6             helper = new Helper();  
7         return helper;  
8     }  
9     ...  
10 }
```

Lazy Initialization In Multi-Threaded Context

```
1 class Foo {  
2     private Helper helper = null;  
3  
4     public Helper getHelper() {  
5         if (helper == null)  
6             helper = new Helper();  
7         return helper;  
8     }  
9     ...  
10 }
```

```
1 class Foo {  
2     private Helper helper = null;  
3  
4     public synchronized Helper getHelper() {  
5         if (helper == null)  
6             helper = new Helper();  
7         return helper;  
8     }  
9     ...  
10 }
```

Synchronizes even after helper has been allocated.
Can we optimize the initialization pattern?

Double-Checked Locking

- (i) Check if helper is initialized
 - ▶ If yes, return
 - ▶ If no, then obtain a lock
- (ii) Double check whether helper has been initialized
 - ▶ Perhaps concurrently initialized in between Steps 1 and 2
- (iii) If yes, return
- (iv) Initialize helper, and return

```
1  class Foo {  
2      private Helper helper = null;  
3  
4      public Helper getHelper() {  
5          if (helper == null) {  
6              synchronized (this) {  
7                  if (helper == null)  
8                      helper = new Helper();  
9              }  
10         }  
11         return helper;  
12     }  
13     ...  
14 }
```

Broken Usage of Double-Checked Locking

```
1  class Foo {  
2      private Helper helper = null;  
3  
4      public Helper getHelper() {  
5          if (helper == null) {  
6              synchronized (this) {  
7                  if (helper == null)  
8                      helper = new Helper();  
9              }  
10         }  
11         return helper;  
12     }  
13     ...  
14 }
```

Not platform-independent
when implemented in Java

Double-Checked Locking: Broken Fix

```
1 class Foo {
2     private Helper helper = null;
3     public Helper getHelper() {
4         if (helper == null) {
5             Helper h;
6             synchronized (this) {
7                 h = helper;
8                 if (h == null) {
9                     synchronized (this) {
10                        h = new Helper();
11                    }
12                }
13                helper = h;
14            }
15        }
16        return helper;
17    }
}
```

- A release operation prevents operations from moving out of the critical section
- It does not prevent `helper = h` (line 7) from being moved up

Correct Use of Double-Checked Locking

```
1  class Foo {
2      private volatile Helper helper = null;
3      public Helper getHelper() {
4          if (helper == null) {
5              synchronized (this) {
6                  if (helper == null)
7                      helper = new Helper();
8              }
9          }
10         return helper;
11     }
12 }
```

Other possibilities are to use barriers in both the writer thread (the thread that initializes helper) and all reader threads

Readers-Writer Locks

Many objects are read concurrently and updated only a few times

Reader lock No thread holds the write lock

Writer lock No thread holds the reader or writer locks

```
1 public interface RWLock {  
2     public void readerLock();  
3     public void readerUnlock();  
4     public void writerLock();  
5     public void writerUnlock();  
6 }
```

Design Choices in Readers-Writer Locks

Release preference order Writer releases lock, both readers and writers are queued up

Incoming readers Writers waiting, and new readers are arriving

Downgrading Can a thread acquire a read lock without releasing the write lock?

Upgrading Can a read lock be upgraded to a write lock?

Readers-Writer Lock

Reader or writer preference impacts degree of concurrency

- Allows starvation of non-preferred threads

```
1 readerLock():
2   acquire(rd)
3   rdrs++
4   if rdrs == 1:
5     acquire(wr)
6   release(rd)
7
8 readerUnlock():
9   acquire(rd)
10  rdrs--
11  if rdrs == 0:
12    release(wr)
13  release(rd)
```

```
14 writerLock():
15   acquire(wr)
16
17 writerUnlock():
18   release(wr)
```

Readers-Writer Lock With Reader-Preference

```
1  class RWLock {
2      int n = 0;
3      const int WR_MASK = 1;
4      const int RD_INC = 2;
5
6      public void writerLock() {
7          while (!CAS(&n, 0, WR_MASK)) {
8              }
9      }
10
11     public void writerUnlock() {
12         FAA(&n, -WR_MASK);
13     }
```

```
14     public void readerLock() {
15         FAA(&n, RD_INC);
16         while ((n & WR_MASK) == 1) {
17             }
18     }
19     public void readerUnlock() {
20         FAA(&n, -RD_INC);
21     }
22 }
```

Lock Implementations in a JVM

All objects in Java are potential locks

Recursive lock lock can be acquired multiple times by the owner

Thin lock spin lock used when there is no contention, inflated to a fat lock on contention

Fat lock lock is contended or is waited upon, maintains a list of contending threads

Asymmetric Locks

Often objects are locked by at most one thread

Biased locks

- JVMs use biased locks, the acquire/release operations on the owner threads are cheaper
- Usually biased to the first owner thread
- Synchronize only when the lock is contended, need to take care of several subtle issues
- `-XX:+UseBiasedLocking` in HotSpot JVM

Monitors

Using Locks to Access a Bounded Queue

- Consider a bounded FIFO queue
- Many producer threads and one consumer thread access the queue

```
1 mutex.lock();  
2 try {  
3     queue.enqueue(x);  
4 } finally {  
5     mutex.unlock();  
6 }
```

What are potential challenges?

Using Locks to Access a Bounded Queue

- Consider a bounded FIFO queue
- Many producer threads and one consumer thread access the queue

```
1 mutex.lock();  
2 try {  
3     queue.enqueue(x);  
4 } finally {
```

- Producers/Consumers need to know about the size of the queue
- The design may evolve, there can be multiple queues, along with new producers/consumers
- Every producer/consumer need to follow the locking convention

Monitors to the Rescue!

- Combination of methods, mutual exclusion locks, and condition variables
- Provides mutual exclusion for methods
- Provides the possibility to wait for a condition (cooperation)
 - ▶ Condition Variables in monitors have an associated queue
 - ▶ Operations: wait, notify (signal), and notifyAll (broadcast)

```
1 public synchronized void enqueue()  
   {  
2     queue.enqueue(x);  
3 }
```

Condition Variables in Monitors

wait var, mutex

- Make the thread wait until a condition COND is true
 - ▶ Releases the monitor's mutex
 - ▶ Moves the thread to var's wait queue
 - ▶ Puts the thread to sleep
- Steps 1–3 are atomic to prevent race conditions
- When the thread wakes up, it is assumed to hold mutex

notify var

- Invoked by a thread to assert that COND is true
- Moves one or more threads from the wait queue to the ready queue

notifyAll var

Moves all threads from wait queue to the ready queue

Signaling Policies

Signal and continue (SC)

- Signaler thread holds the lock
- Java implements SC only

Signal and wait (SW)

- Signaler thread needs to reacquire the lock
- Signaled thread can continue execution

Signal and urgent wait (SU)

- Like SW, but signaler thread gets to go after the signaled thread

Signal and exit (SX)

- Signaler exits, signaled thread can continue execution

Producer-Consumer with Spin Locks

```
1 Queue q;  
2 Mutex mtx;  
3  
4 producer:  
5   while true:  
6     data = new Data(...);  
7     acquire(mtx);  
8     while q.isFull():  
9       release(mtx);  
10    ...  
11    acquire(mtx);  
12    q.enq(data);  
13    release(mtx);
```

```
14 consumer:  
15   while true:  
16     acquire(mtx)  
17     while q.isEmpty():  
18       release(mtx);  
19     ...  
20     acquire(mtx);  
21     data = q.deq();  
22     release(mtx);  
23     ...
```

Producer-Consumer with Monitors

```
1 Queue q;  
2 // Has an associated queue  
3 Mutex mtx;  
4 CondVar empty, full;  
5  
6 producer:  
7   while true:  
8     data = new Data(...);  
9     acquire(mtx);  
10    while q.isFull():  
11      wait(full, mtx);  
12    q.enq(data);  
13    notify(empty);  
14    release(mtx);
```

```
15 consumer:  
16   while true:  
17     acquire(mtx)  
18     while q.isEmpty():  
19       wait(empty, mtx);  
20     data = q.deq();  
21     notify(full);  
22     release(mtx);  
23     ...
```

Semaphore Implementation with Monitors

```
1 int numRes = N;  
2 Mutex mtx;  
3 CondVar zero;  
4  
5 P:  
6     acquire(mtx);  
7     while numRes == 0:  
8         wait(zero, mtx);  
9     assert numRes > 0  
10    numRes --;  
11    release(mtx);
```

```
12 V:  
13     acquire(mtx);  
14     numRes++;  
15     notify(zero);  
16     release(mtx);
```


Reader-Writer Locks with Reader-Preference

```
1 readerLock():
2   acquire(rd)
3   rdrs++
4   if rdrs == 1:
5     acquire(wr)
6   release(rd)
7
8 readerUnlock():
9   acquire(rd)
10  rdrs--
11  if rdrs == 0:
12    release(wr)
13  release(rd)
```

```
14 writerLock():
15   acquire(wr)
16
17 writerUnlock():
18   release(wr)
```

Reader-Writer Locks with Reader-Preference

```
1 readerLock():
2   acquire(rd)
3   rdrs++
4   if rdrs == 1:
5     acquire(wr)
6   release(rd)
7
8 readerUnlock():
9   acquire(rd)
10  rdrs--
11  if rdrs == 0:
12    release(wr)
13  release(rd)
```

```
14 writerLock():
15   acquire(wr)
16
17 writerUnlock():
18   release(wr)
```

How can we construct a Reader-Writer lock with writer-preference?

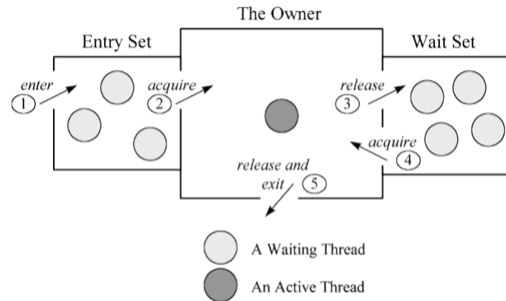
Reader-Writer Lock With Writer-Preference

```
1 readerLock():
2   acquire(global)
3   while writerFlag:
4     wait(writerWait, global)
5   rdrs++
6   release(global)
7
8 readerUnlock():
9   acquire(global)
10  rdrs--
11  if rdrs == 0:
12    notifyAll(writerWait)
13  release(global)
14
```

```
15 writerLock():
16   acquire(global)
17   while writerFlag:
18     wait(writerWait, global)
19   writerFlag = true
20   while rdrs > 0:
21     wait(writerWait, global)
22   release(global)
23
24 writerUnlock():
25   acquire(global)
26   writerFlag = false
27   notifyAll(writerWait)
28   release(global)
```

Monitors in Java

- Java provides built-in support for monitors
 - ▶ synchronized blocks and methods
 - ▶ wait(), notify(), and notifyAll()
- Each object can be used as a monitor



Bounded Buffer with Monitors in Java

```
1 import java.util.concurrent.locks.Condition;
2 import java.util.concurrent.locks.Lock;
3 import java.util.concurrent.locks.ReentrantLock;
4
5 public class BoundedBuffer {
6     private final String[] buffer;
7     private final int capacity; // Constant, length of buffer
8     private int count; // Current size
9     private final Lock lock = new ReentrantLock();
10    private final Condition full = new Condition();
11    private final Condition empty = new Condition();
12    ...
13    public void addToBuffer ();
14    public void removeFromBuffer ();
15    ...
16 }
```

Bounded Buffer with Monitors in Java

```
17 public void addToBuffer() {
18     lock.lock();
19     try {
20         while (count == capacity)
21             full.await();
22         ...
23         empty.signal();
24     } finally {
25         lock.unlock();
26     }
27 }
```

```
28 public void removeFromBuffer() {
29     lock.lock();
30     try {
31         while (count == 0)
32             empty.await();
33         ...
34         full.signal();
35     } finally {
36         lock.unlock();
37     }
38 }
```

References



M. Herlihy et al. The Art of Multiprocessor Programming. Chapters 1, 2, 7–8, 2nd edition, Morgan and Claypool.



M. L. Scott and T. Brown. Shared-Memory Synchronization. Chapters 1–7, 2nd edition, Springer.