

# Demystifying the border of algebraic models

Joint works with Pranjal Dutta & Prateek Dwivedi. [CCC'21, FOCS'21, FOCS'22]

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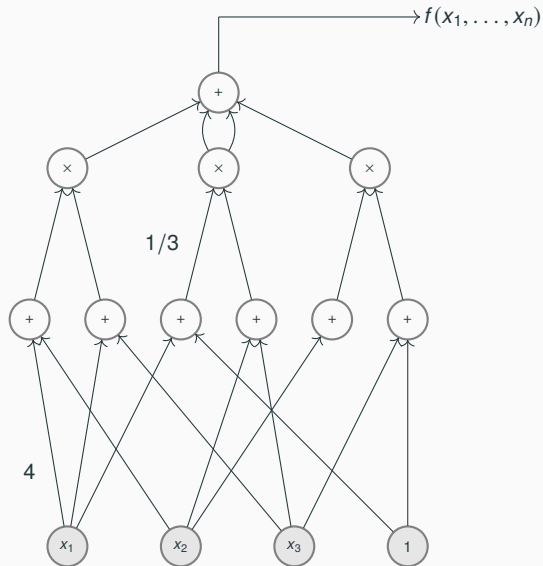
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1. Basic Definitions and Terminologies
2. Border Depth-3 Circuits
3. Proving Upper Bounds
4. Proving Lower Bounds
5. Conclusion

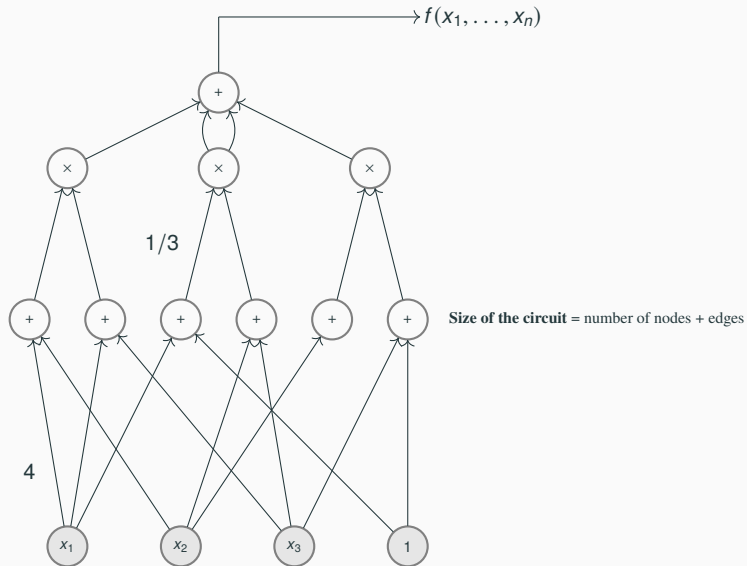
## **Basic Definitions and Terminologies**

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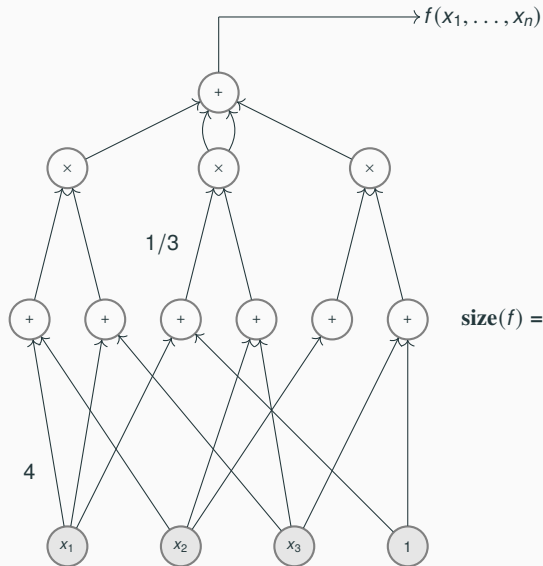
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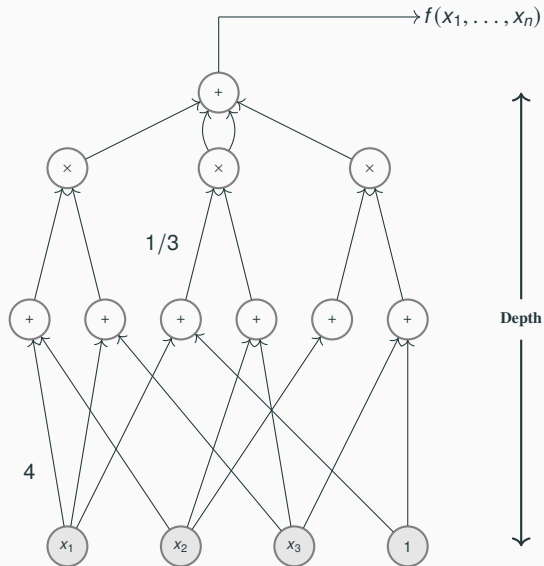


## Algebraic circuits– VP



**$\text{size}(f) = \text{min size of the circuit computing } f$**

## Algebraic circuits– VP







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- Relates tightly to Algebraic Branching Programs **ABP**, or **IMM: Iterated Matrix Multiplication**.

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- ❑ The minimum dimension of the matrix  $X_S$  to compute  $f$ , is called the **permanental complexity**  $\text{pc}(f)$ .



## Valiant's Conjecture– VNP

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Valiant's Conjecture [Valiant 1979]

$\text{VBP} \neq \text{VNP}$  &  $\text{VP} \neq \text{VNP}$ .

Equivalently,  $\text{dc}(\text{perm}_n)$  and  $\text{size}(\text{perm}_n)$  are both  $n^{\omega(1)}$ .

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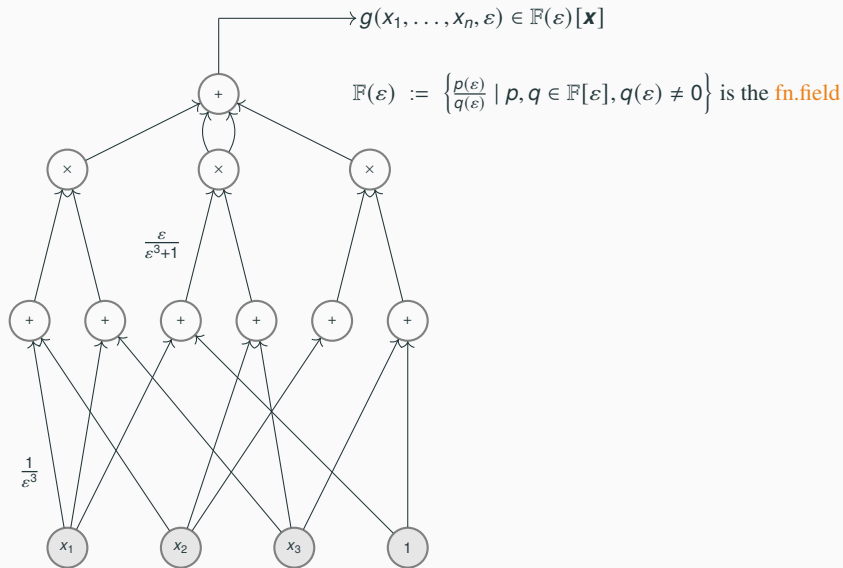
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□ This motivates a new model: ‘*approximative circuit*’.



# Approximative circuits



□ Suppose, we assume the following:

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□ **Bottomline:**  $g_0$  is **non-trivially** ‘approximated’ by the circuit, since  $\lim_{\varepsilon \rightarrow 0} g(\mathbf{x}, \varepsilon) = g_0$ .

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- Curious e.g.: Complexity of degree- $\mathbf{s}$  factor of a size- $\mathbf{s}$  polynomial? [Bhargav-Dwivedi-S. STOC'24] introduces **presentable** border.

## **Border Depth-3 Circuits**

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**Border depth-3 fan-in 2 circuits are ‘universal’ [Kumar 2020]**

Let  $P$  be any  $n$ -variate degree  $d$  polynomial. Then,  $P \in \overline{\Sigma^{[2]}\Pi\Sigma}$ ,

□ Recall:  $h \in \overline{\Sigma^{[k]}\Pi\Sigma}$  of size  $s$  if there exists a polynomial  $g$  such that

$$g(\mathbf{x}, \varepsilon) = h(\mathbf{x}) + \varepsilon \cdot S(\mathbf{x}, \varepsilon) ,$$

where  $g$  can be computed by a  $\Sigma^{[k]}\Pi\Sigma$  circuit, over  $\mathbb{F}(\varepsilon)$ , of size  $s$ .

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Let  $P$  be any  $n$ -variate degree  $d$  polynomial. Then,  $P \in \overline{\Sigma^{[2]}\Pi\Sigma}$ , where the first product has fanin  $\exp(n, d)$  and the second is merely **constant** !

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4. Divide by  $\varepsilon^d$  and rearrange to get

$$P + \varepsilon^d \cdot R(\mathbf{x}, \varepsilon) = -\varepsilon^{-d} + \varepsilon^{-d} \cdot \prod_{i=1}^m \prod_{j=1}^d (\alpha_j + \varepsilon \cdot \ell_i) \in \Sigma^{[2]} \Pi^{[md]} \Sigma .$$

□

## Proving Upper Bounds

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**Remark.** The result holds if one replaces the top-fanin-2 by arbitrary constant  $k$ .

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- We devise a technique called **DiDIL** - **D**ivide, **D**erive, **I**nduct with **L**imit.

- $\text{val}_z(\cdot)$  denotes the highest power of  $z$  dividing it (= least one across monomials). E.g.,  $h = \varepsilon z + \varepsilon^{-1} z^2 x_1 = (\varepsilon z) \cdot (1 + \varepsilon^{-2} z x_1)$ . Then,  $\text{val}_z(h) = 1$ .

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- $\lim_{\varepsilon \rightarrow 0} g_1 = \lim_{\varepsilon \rightarrow 0} \partial_z \Phi(T_1/T_2) = \lim_{\varepsilon \rightarrow 0} \partial_z \Phi(f/T_2).$

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□ Both  $\Phi(T_1)$  and  $\Phi(T_2)$  have  $\Pi\Sigma$  circuits (they have  $z$  and  $\varepsilon$ ).



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- $\deg(f) = d \implies \deg_Z(\Phi(f)) = d \implies \deg_Z(\partial_Z(\Phi(f))) = d - 1$ .
- Suffices to compute  $g_1 \bmod z^d$  and take the limit!

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□ **Note:** *Definite* integration requires setting  $z = 0$  in  $\Phi(T_1/T_2) + 1$ ; that's why we need **power-series** in  $z$ .

□

## Proving Lower Bounds

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# Looking for finer separations

►► skip the section



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- ❑ What lower bound works (if at all!)?

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- ❑ Classical is about *impossibility*. While, border is about *optimality*.

## Proof Idea in $k = 2$ : Non-homogeneity is all we need to care

□ Three cases to consider:

- Case I:  $T_1$  and  $T_2$  each has one linear polynomial  $\ell_i \in \mathbb{F}(\varepsilon)[\mathbf{x}]$  as a factor, whose  $\varepsilon$ -free term is a linear form. Example:  $\ell = (1 + \varepsilon)x_1 + \varepsilon x_2$ ,

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□ For the **first** case, take  $I := \langle \ell_1, \ell_2, \varepsilon \rangle (\Rightarrow 1 \notin I)$  and show that

$$x_1 \cdots x_d + x_{d+1} \cdots x_{2d} + x_{2d+1} \cdots x_{3d} = P_d \bmod I \neq 0,$$

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□ For the **second** case, take  $I := \langle \ell_1, \varepsilon \rangle$ . Then, circuit  $T_1 + T_2 - \varepsilon S \bmod I \in \overline{\Pi\Sigma} = \Pi\Sigma$ , while  $P_d \bmod I \notin \Pi\Sigma$ .

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- For the **first** case, take  $I := \langle \ell_1, \ell_2, \varepsilon \rangle (\Rightarrow 1 \notin I)$  and show that  $x_1 \cdots x_d + x_{d+1} \cdots x_{2d} + x_{2d+1} \cdots x_{3d} = P_d \bmod I \neq 0$ , while circuit  $T_1 + T_2 - \varepsilon S \equiv 0 \bmod I$ .
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- So, **all-non-homogeneous** case is all that remains ...

- $P_d(\mathbf{x}) + \varepsilon \cdot S(\mathbf{x}, \varepsilon) = T_1 + T_2$ , where  $T_i \in \Pi \Sigma \in \mathbb{F}(\varepsilon)[\mathbf{x}]$  have **all-non-homogeneous** linear factors.

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□ Next, partial-derivative **measure**, in  $\mathbf{x}$ , implies  $s \geq 2^{\Omega(d)}!$

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## Conclusion

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Thank you! Questions?