

Demystifying the border of depth-3 circuits

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1. Algebraic Complexity Theory
2. Border Complexity and GCT
3. Border depth-3 circuits
4. Derandomizing border depth-3 circuits
5. Conclusion

Algebraic Complexity Theory

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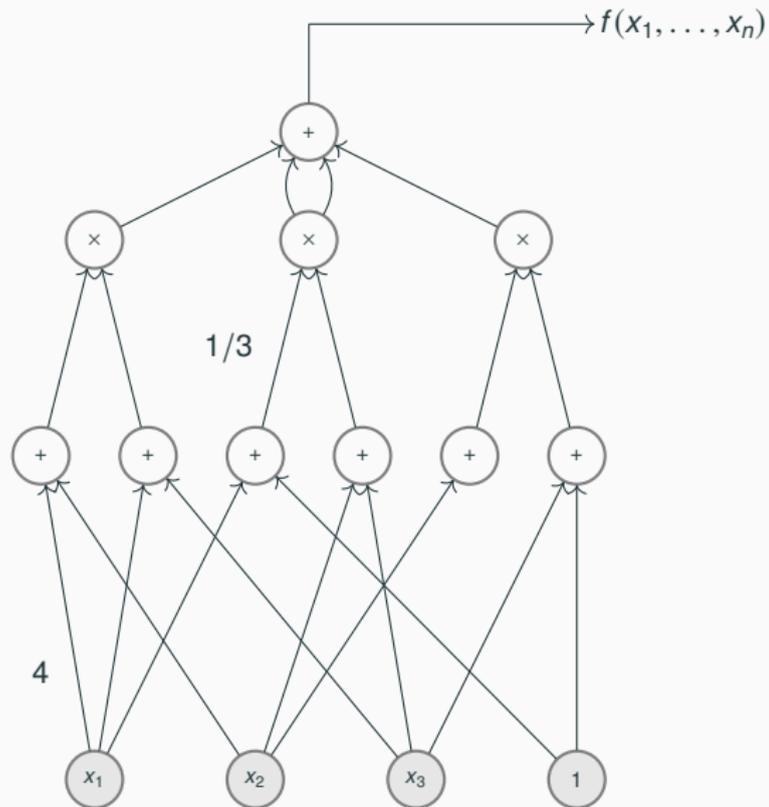
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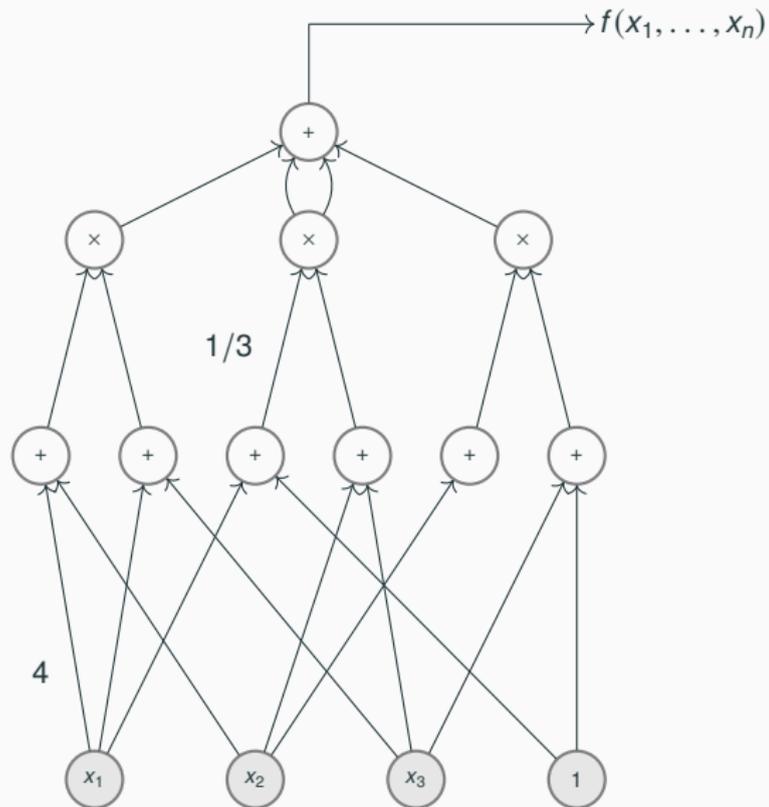
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- ❑ [$P \stackrel{?}{=} NP$, Aaronson 2011] calls GCT "*The String Theory of Computer Science*".

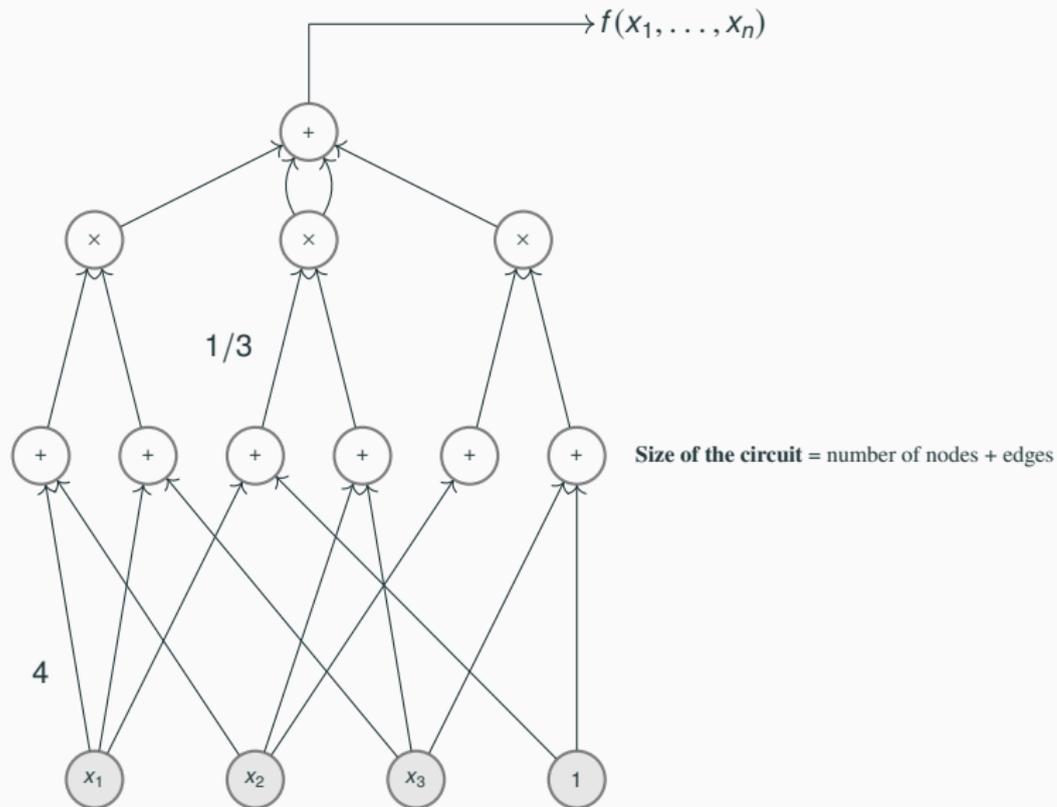
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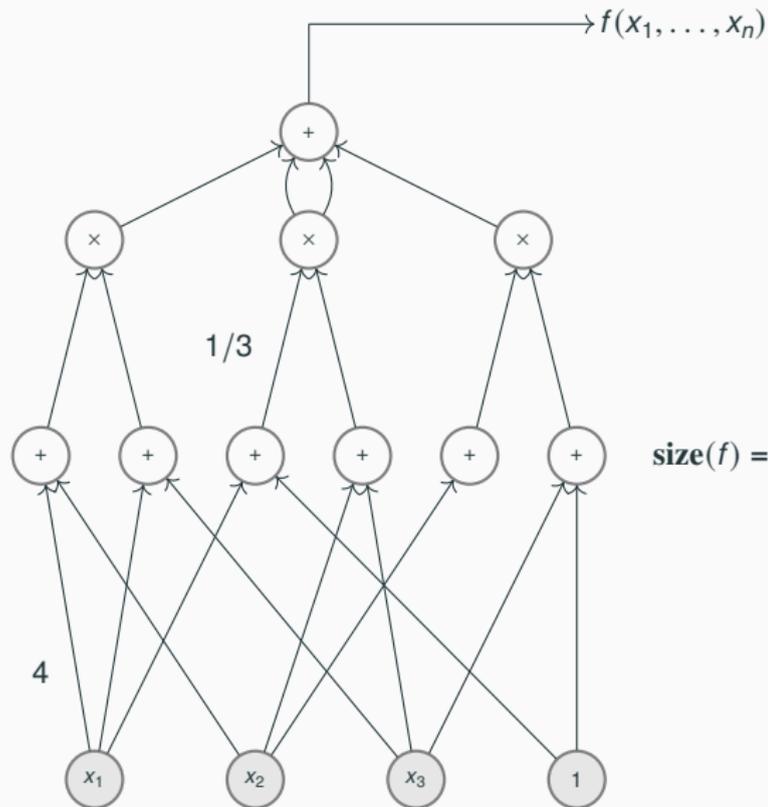
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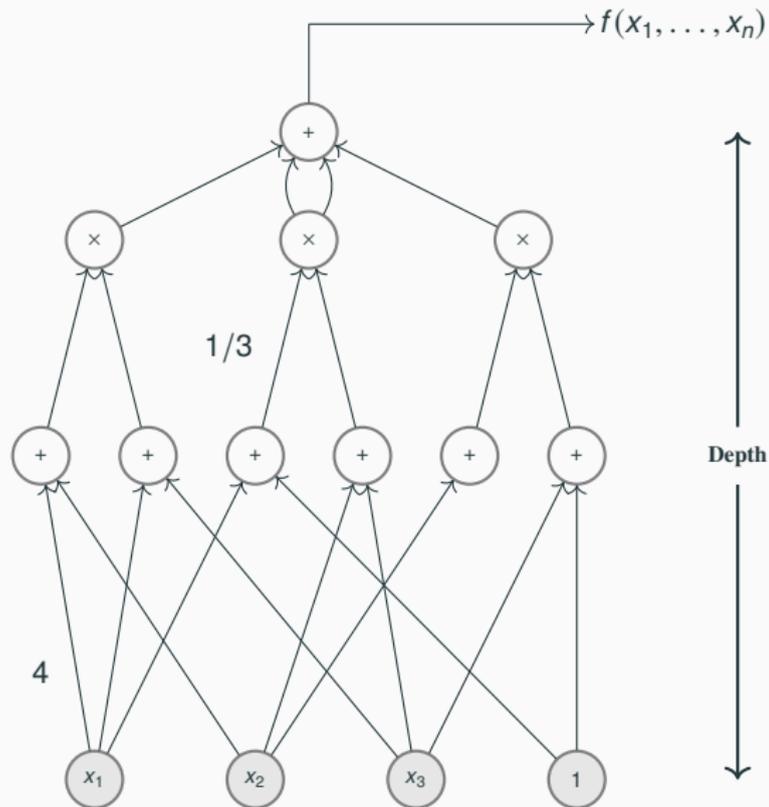


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size(f) = min size of the circuit computing f

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- E.g. $\text{dc}(x_1 \cdots x_n) = n$, since

$$x_1 \cdots x_n = \det \begin{pmatrix} x_1 & 0 & \dots & 0 \\ 0 & x_2 & \dots & 0 \\ \vdots & \vdots & \ddots & \vdots \\ 0 & 0 & \dots & x_n \end{pmatrix}.$$

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- ❑ **Connections**: Linear algebra, Volume, counting planar matchings.

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$\text{VBP} \neq \text{VNP}$ & $\text{VP} \neq \text{VNP}$. Equivalently, $\text{dc}(\text{perm}_n)$ and $\text{size}(\text{perm}_n)$ are both $n^{\omega(1)}$.

[Also, $\text{VBP} \neq \text{VP}$. A candidate?]

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 - Assuming GRH (Generalized Riemann hypothesis), the results hold over \mathbb{C} as well.

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- ❑ Can there be ‘algebraic natural proofs’ to prove $VP \neq VNP$? Some answers: [Chatterjee-Kumar-Ramya-Saptharishi-Tengse 2020, Kumar-Ramya-Saptharishi-Tengse 2020].

Border Complexity and GCT

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- $\text{WR}(h) \leq r$ is denoted as $h \in \Sigma^{[r]} \wedge \Sigma$ (homogeneous *depth-3 diagonal* circuits).

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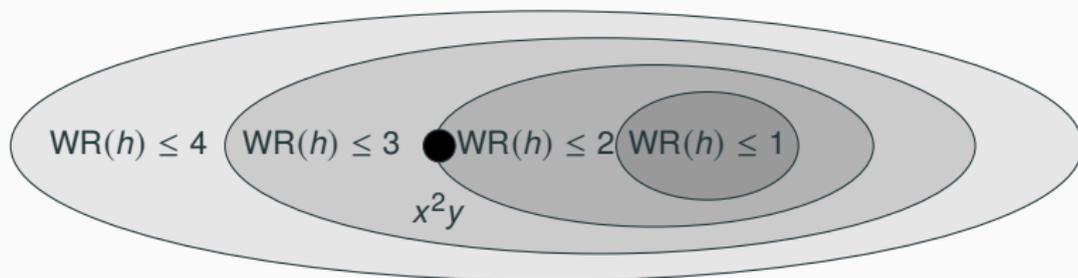
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- ❑ The subtlety is *gone*: $X_n := \{h \mid \overline{\text{WR}}(h) \leq n\}$, is now a **closed** set.

Border Waring rank

The border Waring rank $\overline{\text{WR}}(h)$ is defined as the smallest n such that h can be **approximated** arbitrarily closely by polynomials of Waring rank $\leq n$.

- ❑ $\overline{\text{WR}}(x^2y) = 2$ but $\text{WR}(x^2y) = 3$.
- ❑ The subtlety is *gone*: $X_n := \{h \mid \overline{\text{WR}}(h) \leq n\}$, is now a **closed** set.
- ❑ **On to proving lower bounds**: To show $\overline{\text{WR}}(p) > n$, for some p , it suffices to show that $p \notin X_n$, i.e. find a *continuous* function f that vanishes on X_n but not on p .

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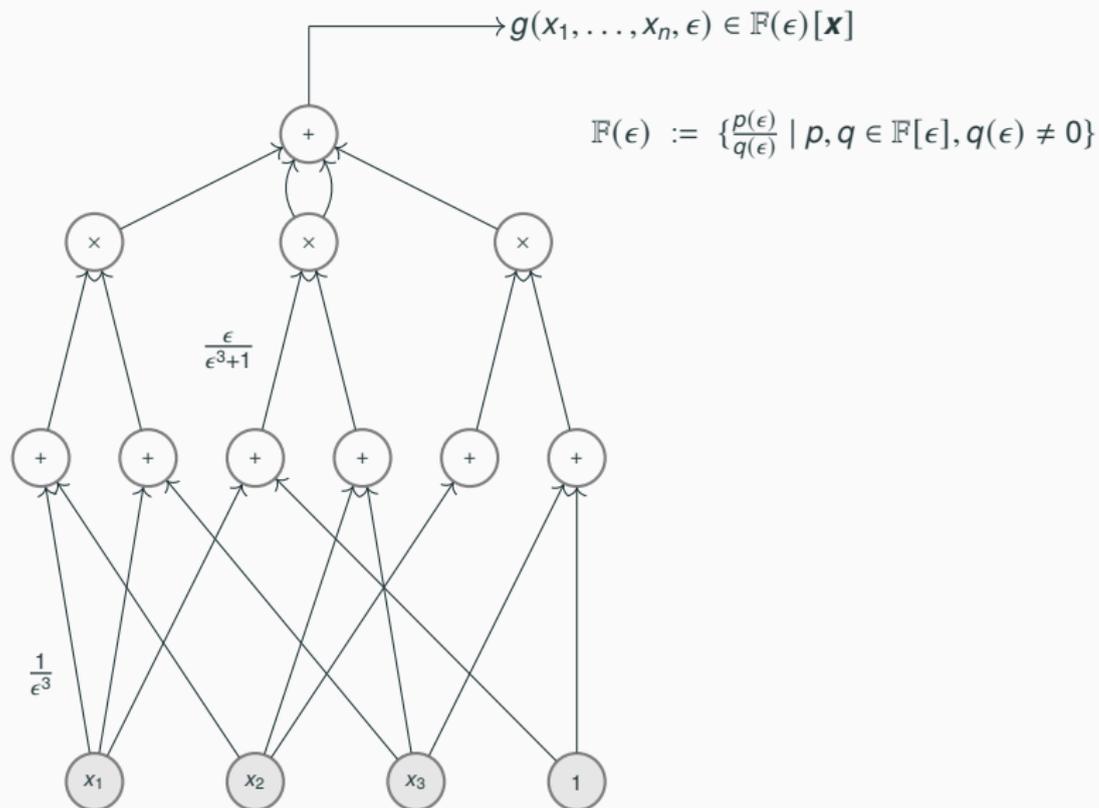
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Border tensor rank is *directly* related to the matrix multiplication exponent ω [Bini 1980, Coppersmith-Winograd 1990].

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- ❑ Coefficients in the earlier definition can be arbitrary depending on the parameter ϵ . Can it be 'nicer'?
- ❑ Yes! Via '*approximative circuits*'.

Approximative circuits (continued)



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□ **Summary:** g_0 is really something **non-trivial** and being ‘approximated’ by the circuit since $\lim_{\epsilon \rightarrow 0} g(\mathbf{x}, \epsilon) = g_0$.

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$VNP \not\subseteq \overline{VBP}$ & $VNP \not\subseteq \overline{VP}$. Equivalently, $\overline{dc}(\text{perm}_n)$ and $\overline{\text{size}}(\text{perm}_n)$ are both $n^{\omega(1)}$.

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- ❑ Both **det** and **perm** have ‘nice’ **symmetries**.
- ❑ Symmetry-characterization **avoids** the **Razborov–Rudich barrier**: *Very few* functions are symmetry-characterized, so symmetry-characterization violates the **largeness** criterion!

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- Further potential applications in identity testing and understanding its ‘robustness’.

Border depth-3 circuits

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- **No.** E.g. the *Inner Product* polynomial $\langle \mathbf{x}, \mathbf{y} \rangle = x_1y_1 + \dots + x_{k+1}y_{k+1}$ **cannot** be written as a $\Sigma^{[k]}\Pi^{[d]}\Sigma$ circuit, *regardless* of the product fanin d !

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Border depth-3 fanin 2 circuits are ‘universal’ [Kumar 2020]

Let P be any homogeneous n -variate degree d polynomial. Then, $P \in \overline{\Sigma^{[2]}\Pi^{[D]}\Sigma}$, where $D := \exp(n, d)$.

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4. Divide by ϵ^d and rearrange to get

$$P + \epsilon^d \cdot R(\mathbf{x}, \epsilon) = -\epsilon^{-d} + \epsilon^{-d} \cdot \prod_{i=1}^m \prod_{j=1}^d (\alpha_j + \epsilon \cdot \ell_i) \in \Sigma^{[2]} \Pi^{[md]} \Sigma .$$

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[Dutta-Dwivedi-Saxena FOCS 2021].

$\overline{\Sigma^{[2]}\Pi^{[d]}\Sigma} \subseteq \text{VBP}$, for $d = \text{poly}(n)$.

In particular, any polynomial in the border of top-fanin-2 size- s depth-3 circuits, can also be exactly computed by a linear projection of a $\text{poly}(s) \times \text{poly}(s)$ determinant.

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[Dutta-Dwivedi-Saxena FOCS 2021].

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In particular, any polynomial in the border of top-fanin-2 size- s depth-3 circuits, can also be exactly computed by a linear projection of a $\text{poly}(s) \times \text{poly}(s)$ determinant.

Remark. The result holds if one replaces the top-fanin-2 by arbitrary constant k .

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- *Infinitely* many factorizations may give *infinitely* many limits.

Proof sketch for $k = 2$

□ $T_1 + T_2 = f(\mathbf{x}) + \epsilon \cdot S(\mathbf{x}, \epsilon)$, where $T_i \in \Pi\Sigma$ in $\mathbb{F}(\epsilon)[\mathbf{x}]$. Assume $\deg(f) =: d$.

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- We devise a technique called DiDIL –
Divide, **D**erive, **I**nterpolate/ Induct with **L**imit.

$k = 2$ proof continued: Divide and Derive

- Let $\Phi(T_i) =: \epsilon^{a_i} \cdot \tilde{T}_i$, for $i \in [2]$, where $a_i := \text{val}_\epsilon(\Phi(T_i))$.
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$$\begin{aligned} f + \epsilon \cdot S &= T_1 + T_2 \\ \implies \Phi(f) + \epsilon \cdot \Phi(S) &= \Phi(T_1) + \Phi(T_2) \\ \implies \Phi(f)/\tilde{T}_2 + \epsilon \cdot \Phi(S)/\tilde{T}_2 &= \Phi(T_1)/\tilde{T}_2 + \epsilon^{a_2} \\ \implies \partial_z \left(\Phi(f)/\tilde{T}_2 \right) + \epsilon \cdot \partial_z \left(\Phi(S)/\tilde{T}_2 \right) &= \partial_z \left(\Phi(T_1)/\tilde{T}_2 \right) =: g_1. \quad (1) \end{aligned}$$

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- $\lim_{\epsilon \rightarrow 0} g_1 = \partial_z(\Phi(f)/t_2)$, where $t_2 := \lim_{\epsilon \rightarrow 0} \tilde{T}_2$.

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$$\begin{aligned}\partial_z \left(\Phi(T_1) / \tilde{T}_2 \right) &= \Phi(T_1) / \tilde{T}_2 \cdot \text{dlog} \left(\Phi(T_1) / \tilde{T}_2 \right) \\ &= (\Pi\Sigma / \Pi\Sigma) \cdot \text{dlog}(\Pi\Sigma / \Pi\Sigma) \\ &= \Pi\Sigma / \Pi\Sigma \cdot \left(\pm \sum \text{dlog}(\Sigma) \right).\end{aligned}$$

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- Here Σ means just a linear polynomial ℓ .

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□ Suffices to compute $\lim_{\epsilon \rightarrow 0} g_1 \pmod{z^d}$.

$k = 2$ proof: dlog strikes!

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Thus,

$$\begin{aligned}\lim_{\epsilon \rightarrow 0} g_1 \pmod{z^d} &\equiv \lim_{\epsilon \rightarrow 0} \Pi\Sigma/\Pi\Sigma \cdot \left(\sum \text{dlog}(\Sigma)\right) \pmod{z^d} \\ &\equiv \lim_{\epsilon \rightarrow 0} (\Pi\Sigma/\Pi\Sigma) \cdot (\Sigma \wedge \Sigma) \pmod{z^d} \\ &\in \overline{(\Pi\Sigma/\Pi\Sigma) \cdot (\Sigma \wedge \Sigma)} \pmod{z^d} .\end{aligned}$$

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Derandomizing border depth-3 circuits

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Theorem 2 (Derandomizing polynomial-sized depth-3 top-fanin- k circuits) [Dutta-Dwivedi-Saxena 2021]

There exists an explicit quasipolynomial-time ($s^{O(\log \log s)}$) hitting set for size- s $\overline{\Sigma^{[k]}\Pi\Sigma}$ circuits, for any constant k .

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