Title: Formal Methods for Software Reliability and Synthesis

Speaker: Ashish Mishra is a Postdoctoral Researcher at Purdue University

Date and Time: Oct 5, 2023 (Thursday) 4-5 PM

Venue: Online

## Abstract:

Building reliable software has been a classical goal in Computer Science. The most basic premise of my research is derived from this goal; Can we make programs safe and reliable using formal techniques, thereby achieving the long-dreamt reliability goals while making programming as a discipline more democratic and accessible to the masses? My current and future research is a step toward answering this question in the affirmative. In this talk, I will begin by highlighting some of these overarching research interests and directions. I will primarily present two of my recent works highlighting the effective use of Refinement types, DSLs, and SMT-based techniques for the verification and synthesis of effectful programs. (i) The first one is a new specification-guided synthesis procedure that uses Hoare-style pre- and post-conditions to express fine-grained effects of potential library component candidates to drive a bi-directional synthesis search strategy. It integrates a conflict-driven learning procedure into the synthesis algorithm that provides a semantic characterization of previously encountered unsuccessful search paths used to prune possible candidates' space as synthesis proceeds. (ii) The second work is a new Refinement-Type system called Coverage Type which adapts the recent work in Incorrectness Logic to the specification and automated verification of test input generators used in modern property-based testing systems. Specifications are expressed in the language of refinement types, augmented with coverage types, types that reflect underapproximate constraints on program behavior.

I will also discuss some of my other works, ongoing works, and future research directions. Particularly, I will discuss three potential paths: (i) A Few fundamental challenges/opportunities in program verification and synthesis and potential solutions to some of these; (ii) Applying program synthesis to novel domains like Robotics. (iii) Combining Neural and Symbolic program synthesis approaches, aka Neurosymbolic program synthesis.

## **Bio:**

Ashish Mishra is a Postdoctoral Researcher at Purdue University, where he works with Professor Suresh Jagannathan in the areas of Programming Languages, Program Verification, and Program Synthesis. Prior to his current position, he worked with Professor Frank Tip on Program Analysis for JavaScript for an year. Ashish obtained his Ph.D. from the Indian Institute of Science, where he worked under the supervision of Professor Y. N. Srikant.

In addition to his work in Computer Science, Ashish is also interested in applying technology to public policies and solving social problems. He is currently involved with several Indian NGOs such as PARI (People's Archive for Rural India), Mosali (a startup trying to bring women into workforce), and others that are involved in Media Monitoring and Research.