

Errata to Long-Latency Branches: How Much Do They Matter?

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The original article titled “Long-Latency Branches: How Much Do They Matter?” was published in the June, 2006 issue (vol. 34, issue 3) of SIGARCH Computer Architecture News. There were three errors in the article. In Figure 2, `clustalw` was shown at a wrong place on the x-axis. The corrected figure along with the original caption is shown below. The second error was related to the number of applications used in the article. At two places the article reported that twenty three applications were used. This number should be twenty two. The third error was related to the SDRAM frequency. It should be 400 MHz instead of 800 MHz.

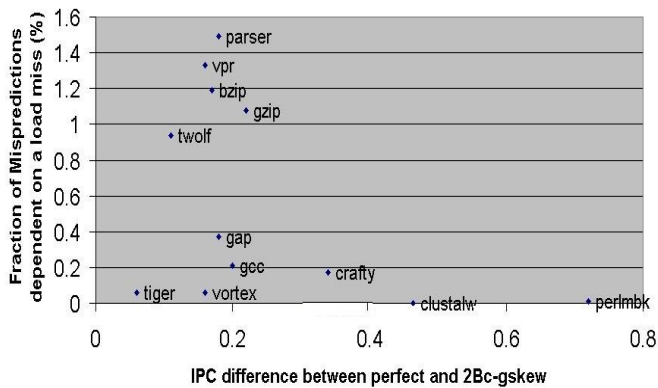


Fig. 1. Distribution of applications based on sensitivity to long-latency branches in a 38-stage pipeline